

FIG. 1

TRANSISTOR GATE AND LOCAL INTER CONNECT

Jigish D. Trivedi

Appl. No.: Unknown Atty Docket: MICRON.104DVI

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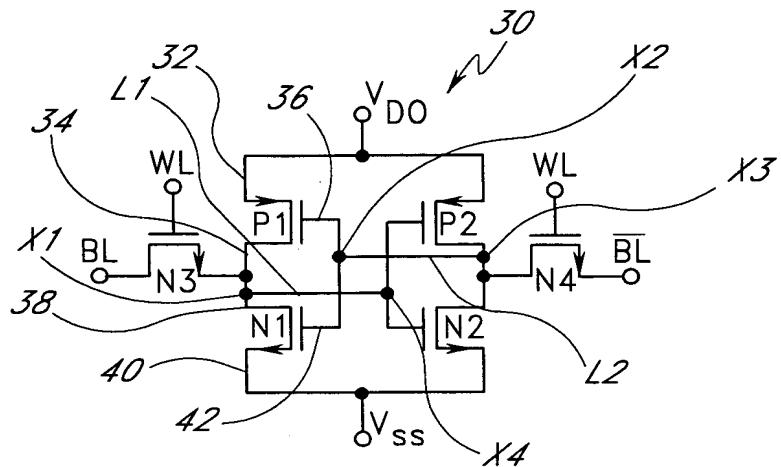


FIG. 2

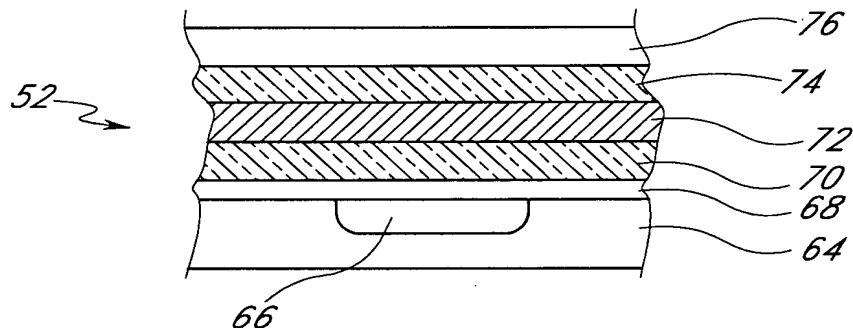


FIG. 3

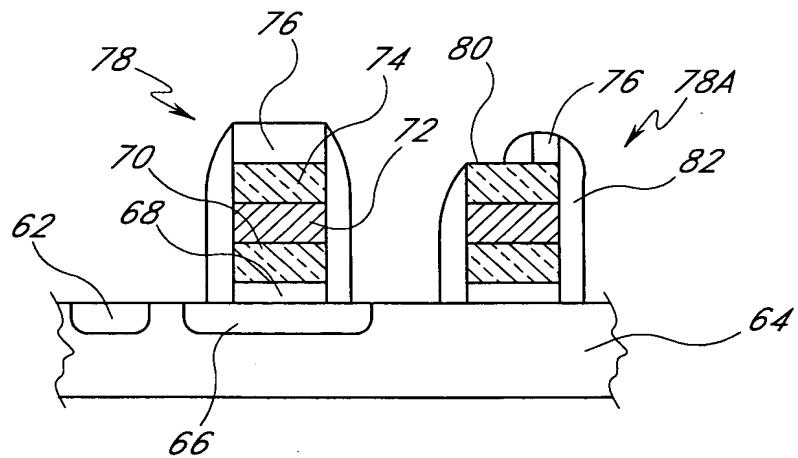


FIG. 4

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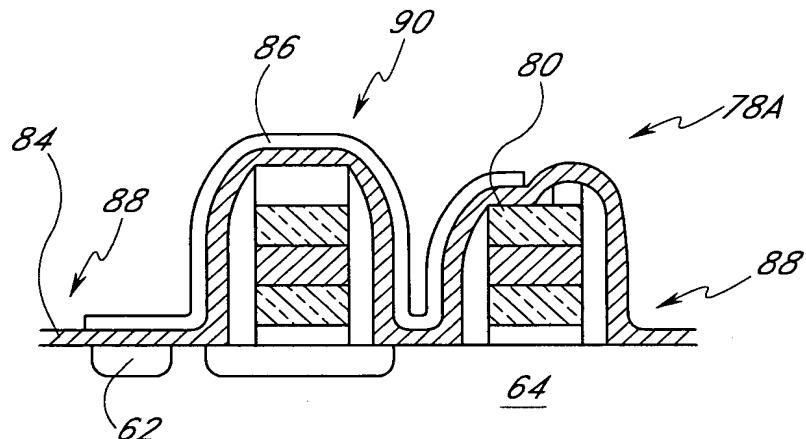


FIG. 5

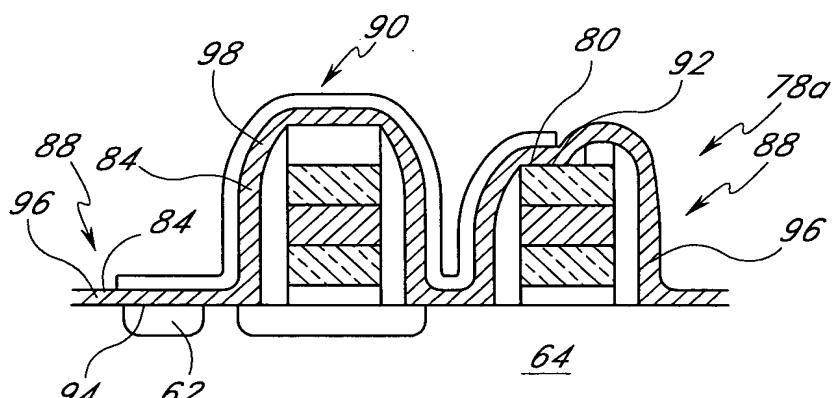


FIG. 6

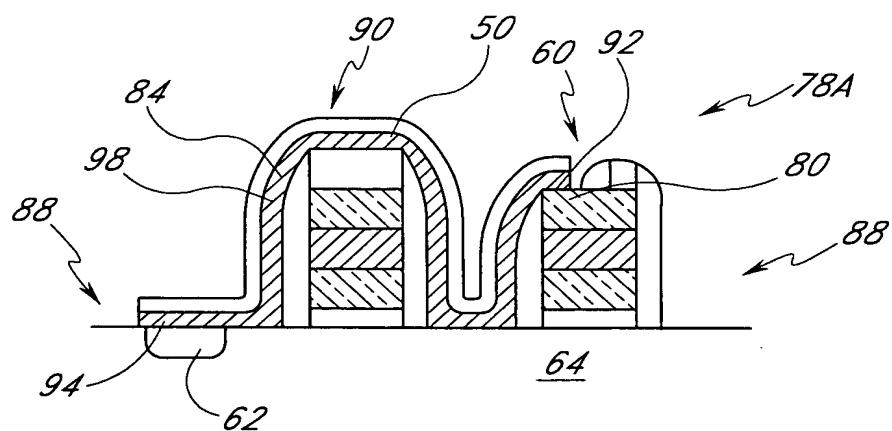


FIG. 7